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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/630,537	07/29/2003	Sandeep Bhatia	CA7034222001	9252
23639 7590 01/24/2007 BINGHAM, MCCUTCHEN LLP THREE EMBARCADERO CENTER 18 FLOOR SAN FRANCISCO, CA 94111-4067			EXAMINER CHUNG, PHUNG M	
			ART UNIT 2138	PAPER NUMBER

SHORTENED STATUTORY PERIOD OF RESPONSE	MAIL DATE	DELIVERY MODE
3 MONTHS	01/24/2007	PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

If NO period for reply is specified above, the maximum statutory period will apply and will expire 6 MONTHS from the mailing date of this communication.

Reprint mail
2-1-07

Office Action Summary

Application No.

10/630,537

Applicant(s)

BHATIA, SANDEEP

Examiner

Phung My Chung

Art Unit

2138

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 30 October 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 15-18 and 24-27 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 15-18 and 24-27 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date <u>9/27/06</u> . | 6) <input type="checkbox"/> Other: _____ |

Claim Rejections - 35 USC § 103 (a)

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 15-18 and 24-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Quayle et al (6,694,464) in view of Kiefer et al (Deterministic BIST with multiple scan chains).

As per claims 15-18, Quayle et al disclose an apparatus comprising:
means for dividing p pins of an integrated circuit into n groups;
means for logically associating the pins of each group; and
means for driving a plurality of scan chains in the integrated circuit with the logically associated pins. (See Fig. 11, col. 16, lines 50-65 and col. 17, lines 42-67 to col. 18, lines 1-25) Quayle et al do not disclose an ExOR matrix. However, Kiefer et al disclose an ExOR matrix (ExOR column). (See section 2. Target structure). Therefore, it would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to incorporate the ExOR matrix as taught by Kiefer et al into the invention of Quayle et al for logically associating each pin of each group through an ExOR matrix to complete fault coverage with less chip area.

As per claims 24-27, these claims are rejected under similar rationale as set forth in claims 15-18.

3. Claims 15-18 and 24-27 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sample et al (6,377,912) in view of Kiefer et al (Deterministic BIST with multiple scan chains).

As per claims 15-18, Sample et al disclose an apparatus comprising:
means for dividing p pins of an integrated circuit into n groups;
means for logically associating the pins of each group; and
means for driving a plurality of scan chains in the integrated circuit with the logically associated pins. (See Fig. 11, col. 14, lines 20-67 and col. 15, lines 1-62)
Sample et al do not disclose an ExOR matrix. However, Kiefer et al disclose an ExOR matrix (ExOR column). (See section 2. Target structure). Therefore, it would have been obvious to a person of ordinary skill in the art, at the time the invention was made, to incorporate the ExOR matrix as taught by Kiefer et al into the invention of Sample et al for logically associating each pin of each group through an ExOR matrix to complete fault coverage with less chip area.

As per claims 24-27, these claims are rejected under similar rationale as set forth in claims 15-18.

4. Applicant's submission of an information disclosure statement under 37 CFR 1.97(c) with the fee set forth in 37 CFR 1.17(p) on 9/27/06 prompted the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 609.04(b). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phung My Chung whose telephone number is 571-272-3818. The examiner can normally be reached on Monday to Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on 571-272-3819. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

A handwritten signature in black ink, appearing to read 'Phung My Chung', with a long horizontal flourish extending to the right.

Phung My Chung
Primary Patent Examiner
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